CLAIMS

What is claimed is:

1. A method comprising:

launching a firmware interface on a processing system;

executing logic in the firmware interface to attempt launching an operating system on the processing system;

detecting an unsuccessful attempt to launch the operating system at the firmware interface; and

initiating a system reset of the processing system by the firmware interface in response to detecting the unsuccessful attempt to launch the operating system.

2. The method of claim 1, the method further comprising:

setting a timer at the firmware interface; and

detecting an unsuccessful attempt to launch the operating system by detecting an expiration of the timer at the firmware interface.

3. The method of claim 2, the method further comprising:

detecting a successful launch of the operating system at the firmware interface; and

disabling timer in response to detecting the successful launch of the operating system.

4. The method of claim 1, the method further comprising re-attempting to launch the operating system on the processing system in response to initiating a system reset of the processing system.

5. The method of claim 1, the method further comprising attempting to launch a different operating system on the processing system in response to a system reset of the processing system.

6. The method of claim 1, the method further comprising launching the firmware interface in response to an execution of one or more BIOS routines.

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7. The method of claim 6, the method further comprising:

launching a boot manager in response to the execution of the one or more BIOS routines; and

launching an operating system loader from the boot manager.

8. A processing system comprising:

logic to detect an unsuccessful launch of an operating system; and

logic to initiate a system reset of the processing system in response to detecting the unsuccessful attempt to launch the operating system.

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9. The processing system of claim 8, the processing system further comprising:

logic to set a timer; and

logic to detect an unsuccessful attempt to launch the operating system by detecting an expiration of the timer.

10. The processing system of claim 9, the processing system further comprising:

logic to detect a successful launch of the operating system; and

logic to disable the timer in response to detecting the successful launch of the operating system.

11. The processing system of claim 8, the processing system further comprising logic to re-attempt launching the operating system on the processing system in response to a system reset of the processing system.

12. The processing system of claim 8, the processing system further comprising logic to attempt launching a different operating system on the processing system in response to a system reset of the processing system.

13. The processing system of claim 8, the processing system further comprising logic to launch a firmware interface in response to an execution of one or more BIOS routines.

14. The processing system of claim 13, the processing system further comprising:

logic to launch a boot manager in response to the execution of the one or more BIOS routines; and

logic to launch an operating system loader from the boot manager.

15. An article comprising:

a storage medium comprising machine-readable instructions stored thereon for:

attempting to launch an operating system on a processing system;

detecting an unsuccessful attempt to launch the operating system; and

initiating a system reset of the processing system in response to detecting the unsuccessful attempt to launch the operating system.

16. The article of claim 15, wherein the storage medium further comprises machine-readable instructions stored thereon for:

setting a timer; and

detecting an unsuccessful attempt to launch the operating system by detecting an expiration of the timer.

17. The article of claim 16, wherein the storage medium further comprises machine-readable instructions stored thereon for:

detecting a successful launch of the operating system; and

disabling the timer upon detecting the successful launch of the operating system.

18. The article of claim 15, wherein the storage medium further comprises machine-readable instructions stored thereon for re-attempting to launch the operating system on the processing system in response to the system reset of the processing system.

19. The article of claim 15, wherein the storage medium further comprises machine-readable instructions stored thereon for attempting to launch a different operating system on the processing system in response to the system reset of the processing system.

20. The article of claim 15, wherein the storage medium further comprises machine-readable instructions stored thereon for launching a firmware interface in response to an execution of one or more BIOS routines.

21. The article of claim 20, wherein the storage medium further comprises machine-readable instructions stored thereon for:

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launching boot manager in response to the execution of the one or more BIOS routines; and

launching an operating system loader from the boot manager.

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22. A system comprising:

a data bus;

one or more peripheral devices; and

a host processing system coupled to the one or more peripheral devices through the data bus, the host processing system comprising:

logic to detect an unsuccessful launch of an operating system; and

logic to initiate a system reset of the processing system in response to detecting the unsuccessful attempt to launch the operating system.

23. The system of claim 22, wherein the host processing system further comprises:

logic to set a timer; and

logic to detect an unsuccessful attempt to launch the operating system by detecting an expiration of the timer.

24. The system of claim 23, wherein the host processing system further comprises:

logic to detect a successful launch of the operating system; and

logic to disable the timer in response to detecting the successful launch of the operating system.

25. The system of claim 22, wherein the host processing system further comprises logic to re-attempt launching the operating system on the processing system in response to a system reset of the processing system.

26. The system of claim 22, wherein the host processing system further comprises logic to attempt launching a different operating system on the processing system in response to a system reset of the processing system.

27. An apparatus comprising:

means for attempting to launch an operating system on a processing system;

means for detecting an unsuccessful attempt to launch the operating system; and

means for initiating a system reset of the processing system by the firmware interface in response to detecting the unsuccessful attempt to launch the operating system.

28. The apparatus of claim 27, the apparatus further comprising:

means for setting a timer; and

means for detecting an unsuccessful attempt to launch the operating system by detecting an expiration of the timer.

29. The apparatus of claim 28, the apparatus further comprising:

means for detecting a successful launch of the operating system at the firmware interface; and

means for disabling timer in response to detecting the successful launch of the operating system.

30. The apparatus of claim 27, the apparatus further comprising means for re-attempting to launch the operating system on the processing system in response to initiating a system reset of the processing system.

31. The apparatus of claim 2, the apparatus further comprising means for attempting to launch a different operating system on the processing system in response to a system reset of the processing system.